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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/540,953	06/27/2005	Etienne Annic	5284-61PUS	4050
27799 7590 01/22/2009 COHEN, PONTANI, LIEBERMAN & PAVANE LLP 551 FIFTH AVENUE SUITE 1210 NEW YORK, NY 10176				
EXAMINER MITCHELL, DANIEL D				
ART UNIT		PAPER NUMBER		
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/540,953

Applicant(s)

ANNIC, ETIENNE

Examiner

DANIEL MITCHELL

Art Unit

2419

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 October 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 June 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/CDC)
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date: _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____
- Paper No(s)/Mail Date: _____

DETAILED ACTION

Response to Amendment

1. Applicant's amendment filed on October 27, 2008 has been entered. Claims 1-8 have been amended. No claims are canceled. Claims 1-8 are still pending in this application, with claims 1 and 8 being independent.

Response to Arguments

2. Applicant's arguments, see page 9 lines 7-13, filed see October 27, 2008, with respect to the rejection(s) of claim(s) 1-8 under 35 USC 103 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Joeressen(US Patent No. 7,039,031 B1); Venteicher et al. (US Publication No. 2004/0062262 A1); and Downs et al. (US Patent No. 6,249,836 B1), where the new rejection discloses a mobile terminal that manages and executes multiple simultaneous connections to different networks.

3.

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 1-6 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Joeressen (US Patent No. 7,039,031 B1), hereinafter referred as Joeressen , in view of Venteicher et al. (US Publication No. 2004/0062262 A1), hereinafter referred as Venteicher.

Regarding claim 1-7, Joeressen discloses **figure 4** a system for managing a resource in a multi-access point name (APN) terminal for a plurality of architectures each dedicated to a corresponding one of a plurality of communications networks **[mobile terminal for simultaneously operating in multiple network, col. 2 lines 20-21],**

wherein said system comprises a plurality of dedicated architecture resource managers **[figure 4 element 62 and 40]** each configured to process, on behalf of the each architecture, a request defined by a process manager of the each architecture for access to a common resource of the multi-APN terminal **[col. 5 lines 8-25 teaches each architecture having separate processors, hardware interfaces, and memory],** the request being generated as a function of an application activated on said multi-APN terminal, and wherein said each architecture resource manager **[one of the processor from each of the architecture is a resource manager for the architecture, col. 5 lines 8-25]** is configured to dialogue with a resource administrator **[80 figure 4]** of a dedicated architecture manager of the multi-APN terminal to manage the common resource of said multi-APN terminal based on simultaneous operational processing of said

plural dedicated architectures of said multi-APN terminal which are each connected to the corresponding one of said plural communications networks **col. 6 lines 28-40 discloses control managing resources for simultaneous operation in multiple networks].**

However Joeressen does not expressly disclose generating a request as a function of application being activated on the terminal.

Venteicher discloses in **par. 31** a resource being requested when an application is executed on a terminal.

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the teachings of Joeressen to include a generating a request for resources. One would be motivated as such in order to be able to manage multiple connections in a cost efficient manner **par. 7.**

Regarding claim 2, Joeressen disclose wherein each of said plural dedicated architecture resource managers is integrated in each said plural dedicated architectures of said multi-APN terminal **[col. 5 lines 8-28 teaches each dedicated architecture has a processor integrated within each architecture].**

Regarding claim 3, Joeressen discloses wherein each of said plural dedicated architecture resource managers **[processors col. 5 lines 8-28]** includes an interface for exchanging information with said resource administrator

of said dedicated architecture manager **[control unit 80 figure 4 serves as the resource administrator]**.

Regarding claim 4, Joressen discloses wherein each of said plural dedicated architecture resource managers includes an interface for exchanging information with the process manager of each of said plural dedicated architectures **[processors that communicate with other processors in the dedicated architecture, col. 5 lines 8-28]**.

Regarding claim 5, Joressen discloses wherein said resource administrator **[control unit 80, figure 4]** of said dedicated architecture manager of the multi-APN terminal includes an interface for exchanging information with a resource allocator **[controller 60 functions as a resource allocator and interfaces with control unit 80, as disclosed in col. 5 lines 8-25]** of said multi-APN terminal.

Regarding claim 6, Joressen discloses wherein said resource administrator **[control unit 80, figure 4]** of said dedicated architecture manager of the multi-APN terminal includes an interface for exchanging information with a radio interface **[figure 4 discloses air interfaces for radio communication]**.

Regarding claim 8, Joeressen discloses a method of managing a resource in a multi-access point name terminal for a plurality of architectures each

dedicated to and connected to a corresponding one of a plurality of communications networks, method comprising:

Joeressen discloses allocating, at a resource allocator of said multi-APN terminal, the requested resource **[control 60 functions as the resource allocator col. 5 lines 8-25]** and allocating, at a radio interface for accessing said plural communications networks, the requested common resource**[control 60 functions as the resource allocator col. 5 lines 8-25]**.

However Joeressen does not expressly disclose activating an application on said multi-APN terminal, defining, at process managers each associated with a corresponding one of said plural dedicated architectures, defining a common resource corresponding to said application; requesting, at one of said process managers, access to said common resource through a corresponding one of a plurality of dedicated architecture resource managers each associated with a corresponding one of the dedicated architectures; generating, at said one dedicated architecture resource manager: a response after checking said common resource access request; generating the response, at a resource administrator of a dedicated architecture manager of the multi-APN terminal, after checking said common resource access request against simultaneous common resource access requests from others of the plural dedicated architectures of the multi-APN terminal; associating with said application, at said one of the plural dedicated architecture resource

managers, access to the requested common resource after validation of the common resource access request; and executing, at said one process manager, said application by way of said requested common resource.

Venteicher discloses activating an application for a specified resource and connection of the terminal **par. 31**; generating a request to a data link resource manager **par. 31** ; a resource manager makes a decision on available resources and a resource manager makes a decision on available resources of all resources of the terminal **par. 34 data link manager provisions resources based on over all availability**; associating and executing the application with the resources connection **par. 34 data link manager will associate the application with the connection**

See similar motivation as claim 1.

6. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Joeresson and Venteicher in view of Downs et al. (US Patent No. 6,249,836 B1), hereinafter referred as Downs.

Joeressen and Venteicher disclose a system as to the parent claim. **However Joeressen and Venteicher do not expressly disclose wherein each of said plural dedicated architecture resource managers includes a resource correspondence table for defining the resource corresponding to the application activated on said multi-APN terminal.**

Downs discloses including in the memory of an architecture, a resources table corresponding to activated applications **[figure 3 and col. 4 lines 4-9]**

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to modify the teachings of Joeressen and Venteicher to include a memory with a resource management table. One would be motivated as such in order to efficiently manage the resources assigned to a particular application **col. 4 lines 4-9.**

Conclusion

7. Any response to this action should be **faxed** to (571) 173-8300 or **mailed** to:

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Hand delivered responses should be brought to:

Customer Service Window
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DANIEL MITCHELL whose telephone number is (571)270-5307. The examiner can normally be reached on Monday - Friday 8:00 am - 5:00 pm EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chirag G. Shah can be reached on 571-272-3144. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/D. M./
Examiner, Art Unit 2419

/Chirag G Shah/

Supervisory Patent Examiner, Art Unit 2419